(Drawings)

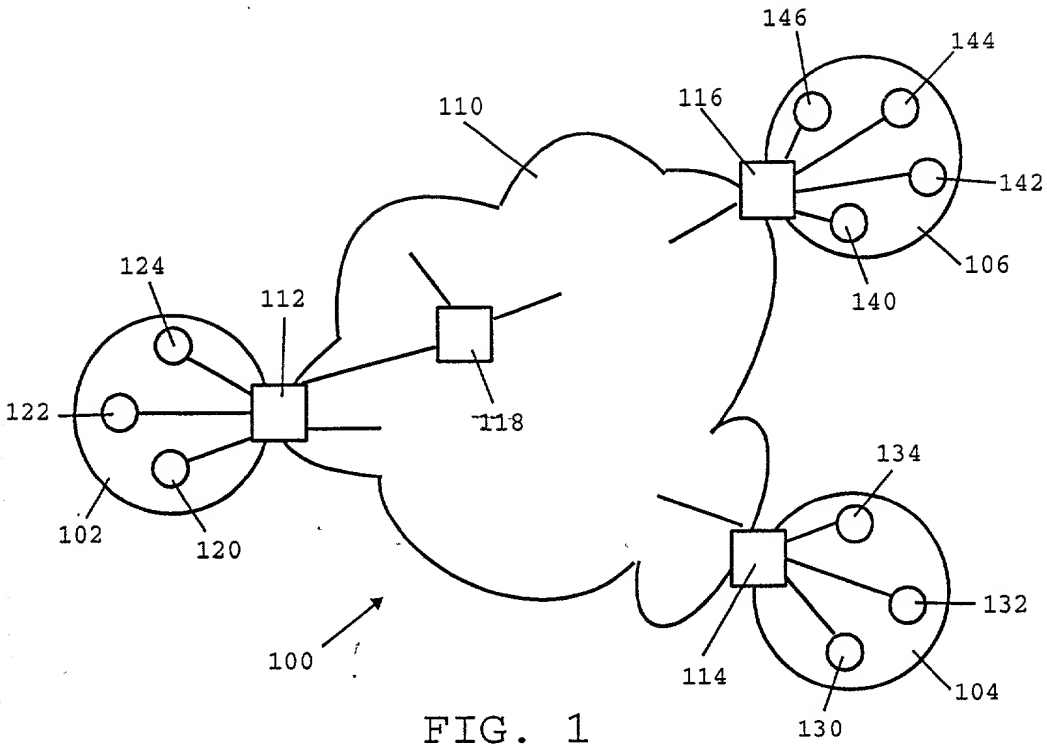

FIG. 1

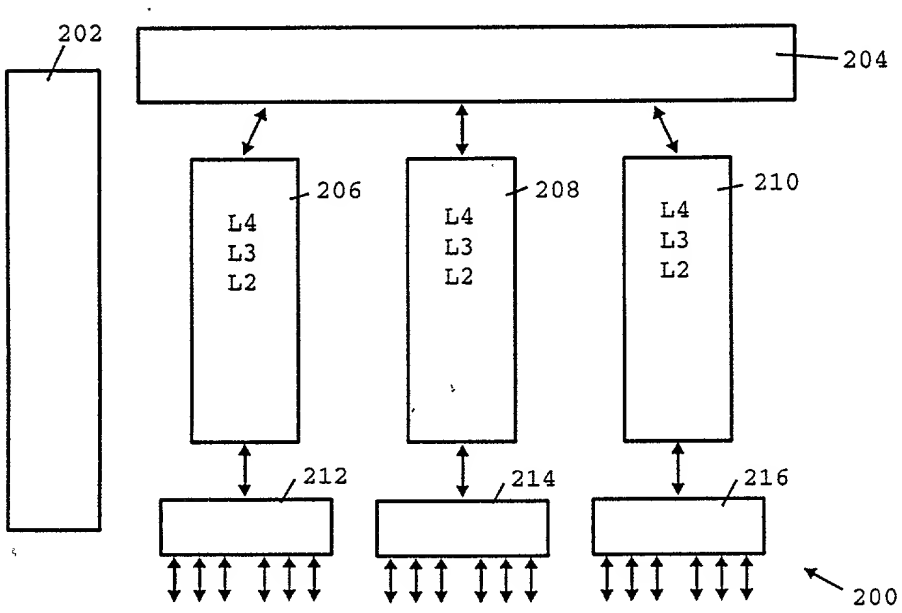

FIG. 2

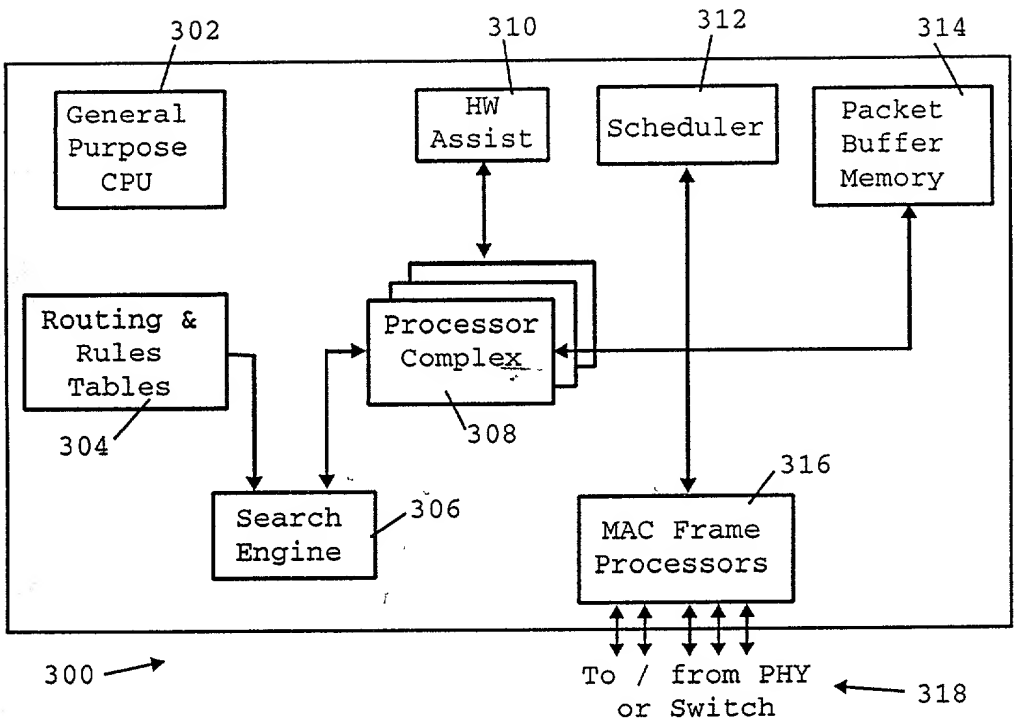
302
310
312
314
General Purpose CPU
HW Assist
Scheduler
Packet Buffer Memory
Routing & Rules Tables
304
Processor Complex
308
Search Engine
306
316
MAC Frame Processors
300
To / from PHY or Switch
318

FIG. 3